| TRANSMITTAL OF FORMAL DRAWINGS                                |             |                |              | Docket No. BUR920020143US1 |                  |
|---|-------------|----------------|--------------|----------------------------|------------------|
| In Re Application Of:  Jacunski, et al.  APR 2 8 2006         |             |                |              |                            |                  |
| Application No.   | Filing Date | FRA BRANCHINET | Customer No. | Group Art Unit             | Confirmation No. |
| 10/604,168  | 06/29/2003  | Mai, Son Luu   | 30449        | 2827                       |                  |
| Invention: TIMER LOCKOUT CIRCUIT FOR SYNCHRONOUS APPLICATIONS |             |                |              |                            |                  |

Address to:

**Commissioner for Patents** P.O. Box 1450 Alexandria, VA 22313-1450

Transmitted herewith are:

4 sheets of formal drawing(s) for this application.

 $\boxtimes$ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).

Jack P. Friedman Registration No. 44,688

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Dated: 02-21-06

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

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